

EAST - (10057172.wsu.1)

File View Edit Tools Window Help

Active

13: (66) (read adj only) with (combinatorial adj logic)

Failed

Saved

(6648) (combin57 near3 logic) and (ROM readonly read-only (read (6474) binary near3 logic\$2 near3 function) (two near3 address (597) ((combin57 near3 logic) and (ROM readonly read-only (read (6474) (binary near3 logic\$2 near3 function) or (two near3 addr (155) (((combin57 near3 logic) and (ROM readonly read-only (rea (500) (((combin57 near3 logic) and (ROM readonly read-only (rea (136) (((combin57 near3 logic) and (ROM readonly read-only (re (7) (combin57 near2 logic) and (binary near3 logic\$2 near3 func (2) ("4849904").pn. (1) 10/172,590 (1) ((read adj only) adj3 data) near3 stor\$3 near3 (combinatori (1) ((read adj only) adj3 data) with stor\$3 with (combinatorial (17549) combin57 adj2 logic (3883) combinatorial adj2 logic (59) (combinatorial adj2 logic) and ((read adj only) adj3 data) (55) ((combinatorial adj2 logic) and ((read adj only) adj3 data (1) (((combinatorial adj2 logic) and ((read adj only) adj3 data (27) (((combinatorial adj2 logic) and ((read adj only) adj3 dat (1) (((combinatorial adj2 logic) and ((read adj only) adj3 dat

Favorites

Tagged (6)

UDC

USPAT. US PGFUS: EPO: JPO: DERWENT: IBM: TOS

Search scope: OR

(read adj only) with (combinatorial adj logic)

BR3 term

SAR term

Image

Text

HTML

	U	1	Document ID	Issue Date	Pages	Title	Current GR	Current XRef	Retrieval C	Inventor	S	C	P	2	3	
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20040044874 A1	20040304	68	Processing devices with improved addressing	711/220	709/220; 710/22;		Leach, Jerald G. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20030233219 A1	20031218	62	Method and apparatus emulating read only memories	703/14			Landers, George et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20030142555 A1	20030731	13	System and method for small read only data	365/189.02			Rarick, Leonard D.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	US 20030056081 A1	20030320	67	Processing devices with improved addressing	711/220	710/42		Leach, Jerald G. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	US 20030041083 A1	20030227	44	Method and apparatus for high speed calculation of	708/622	708/404		Jennings, Earle Willis III et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input type="checkbox"/>	US 20020087745 A1	20020704	11	COMMUNICATIONS INTERFACE AND CONFLICT AVOIDANCE USING A	710/1			CHEN, PETER C.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6625719 B2	20030923	65	Processing devices with improved addressing	711/220			Leach, Jerald G. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6584520 B1	20030624	8	Method for dynamically generating a file allocation	710/68	710/5; 711/206		Cowart, Richard et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6411984 B1	20020625	63	Processor integrated circuit	709/200	709/237		Leach, Jerald G. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	<input type="checkbox"/>	<input type="checkbox"/>	US 6049876 A	20000411	9	Data processing system and method which detect	713/200	711/163		Moughanni, Claude et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	<input type="checkbox"/>	<input type="checkbox"/>	US 5983328 A	19991109	32	Data processing device with time-multiplexed memory bus	711/157	711/150		Potts, James F. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
12	<input type="checkbox"/>	<input type="checkbox"/>	US 5907864 A	19990525	31	Data processing device with time-multiplexed memory bus	711/169			Potts, James F. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

File View Edit Tools Window Help

Details

HTML

Ready

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